

DAC014N120ZZ3

Silicon Carbide Enhancement Mode MOSFET

Features

- High blocking voltage with low Rds(on)
- High frequency operation with low Capacitance
- Simple to drive with -4V/+15V gate
- · Robust body diode with low Qrr
- 100% Avalanche Tested

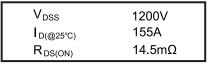
Benefits

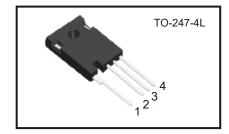
- · Superior robustness and system reliability
- · Higher system efficiency
- · Easier paralleling without thermal runaway
- Capable of high temperature application
- · Faster and more efficient switching

Applications

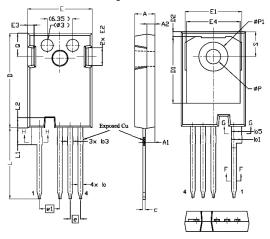
- · EV motor drives
- EV/HEV charging station
- Energy storage and Battery charging
- High voltage DC-DC converters
- Solar / Wind Inverters
- UPS and PFC

G (4) SS (3) S (2)





Package Dimensions



nva an or	DIMENSIONS			DV41DOL	DIMENSIONS			
SYMBOL	MIN.	NOM.	MAX.	SYMBOL	MIN.	NOM.	MAX.	
A	4.83	5.02	5.21	Ε	15,75	15,94	16,13	
A1	2.29	2.41	2.54	E1	13.10	14.02	14.15	
A2	1.91	2.00	2.16	E2	3.68	4.40	5.10	
p,	1.07	1.20	1.28	E3	1.00	1.45	1.90	
ь	1.07	1.20	1.33	E4	12,38	13,26	13,43	
b1	2.39	2.67	2.94	0	2.54 BSC			
b2	2.39	2.67	2.84	e1	5.08 BSC			
b3	1.07	1.30	1.60	L	17.31	17.57	17.82	
b4	1.07	1.30	1.50	L1	3,97	4,19	4,37	
b5	2,39	2.53	2.69	L2	2.35	2.50	2.65	
b6	2.39	2.53	2.64	ØP	3.51	3.61	3.65	
c	0.55	0.60	0.68	ØP1	7.19 REF.			
c1	0,55	0,60	0,65	Q	5,49	5,79	6,00	
D	23.30	23.45	23.60	S	6.04	8.17	6.30	
D1	16.25	16.55	17.65					

Absolute Maximum Ratings

(Tc = 25°C unless otherwise specified)

Parameter	Symbol	Ratings	Unit	
Drain-Source Voltage	V _{GS} =0V I _D =100µA	V_{DS}	1200	V
Gate-Source Voltage (dynamic) duty	f>1 Hz, cycle<1%, e width<200ns)	V_{GS}	-8/+19	V
Gate-Source Voltage (static)	$V_{GS(op)}$	-4/+15	V	
I Drain Current-Continuous	V@ T _C =25°C V@ T _C =100°C	I _D	155 110	Α
Pulse Drain Current		I _{D,pulse}	313	Α
Power Dissipation	P _D	652	W	
Storage Temperature Range		T _{STG}	-55 to +175	°C
Operating Junction Temperature Ra	rating Junction Temperature Range T _J -55 to +175			°C
Soldering Temperature		TL	260	°C
Avalanche Capability, single pulse *	V _{DD} =100V V _{GS} =15V L=2mH	I _{AV}	55	Α
Avalanche Capability, single pulse**	V _{DD} =100V V _{GS} =15V L=2mH	E _{AV}	3025	mJ

^{* 100%} tested in 60% rating

^{** 100%} tested in 36% rating



DAC014N120ZZ3

Electrical Characteristics @ Tc =25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
OFF Characteristics			1	Į.		
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , I _D =0.1mA	1200	-	-	٧
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} =0V , V _{DS} =1200V	-	0.5	60	μΑ
Gate-Source Leakage Current	I _{GSS}	V _{GS} =15V , V _{DS} =0V	-	5	100	nA
ON Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 30 \text{mA}$	1.8	2.5	3.2	٧
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =15V , I _D =75A	-	14.5	19	mΩ
Transconductance	g fs	V _{DS} =20V , I _D =75A	-	90	-	S
Internal Gate Resistance	R _{G(int.)}		3.8	4.3	5.3	Ω
Dynamic Characteristics				l.	,	
Input Capacitance	C _{iss}	V _{DS} =1000V	-	6550	-	
Output Capacitance	C _{oss}			202	-	pF
Reverse Transfer Capacitance	C _{rss}	V _{AC} =25mV	-	10	-	
Coss Stored Energy	E _{oss}	Freq.=100kHz	-	125	-	μJ
Turn-On Switching Energy	E _{on}	V _{DD} =800V , V _{GS} =-4V/+15V	-	1050	-	μJ
Turn-Off Switching Energy	E _{off}	I _D =75A,R _{G(ext)} =2.0Ω L=200μH	-	350	-	
Switching Characteristics				l.	,	
Turn-On Delay Time	$\mathbf{t}_{d(on)}$	V _{DS} =800V	-	16	-	ns
Rise Time	t _r	V _{GS} =-4/+15V I _D =75A	-	37	-	
Turn-Off Delay Time	t _{d(off)}	$R_{G(ext)} = 2.0\Omega$	-	67	-	
Fall Time	t _f	L=200µH	-	13	-	
Total Gate Charge	Qg	V _{DS} =800V	-	235	-	nC
Gate to Source Charge	Q _{gs}	V _{GS} =-4/+15V	-	74	-	
Gate to Drain Charge	\mathbf{Q}_{gd}	I _D =75A	-	73	-	
Body Diode Characteristics				'		
Inverse Diode Forward Voltage	V _{SD}	V _{GS} =-4V , I _{SD} =40A T _J =25°C	-	4.2	-	V
Continuous Diode Forward Current	Is	V _{GS} =-4V , T _J =25°C	-	128	-	Α
Reverse Recovery Time	T _{rr}	V _{GS} =-4V	-	25	-	ns
Reverse Recovery Charge	Qrr	I _{SD} =75A,V _{DS} =800V, dif/dt=2400A/µs	-	670	-	nC
Peak Reverse Recovery Current	I _{rrm}	T _J =25°C	-	45	-	Α
Thermal Resistance		1	<u> </u>	<u> </u>		
Thermal Resistance, Junction-to-Case	$R heta_Jc$		-	0.21	0.23	°C/W

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Fig 1. Output Characteristics, T_J = -40°C

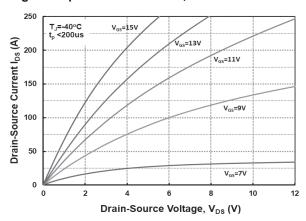


Fig 2. Output Characteristics, T_J = 25°C

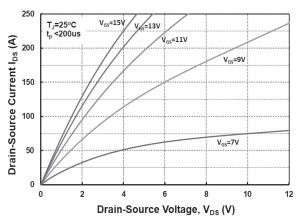


Fig 3. Output Characteristics, T_J = 175°C

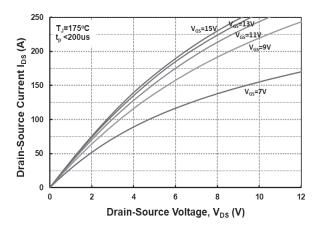


Fig 4. Normalized On-Resistance vs. Temperature

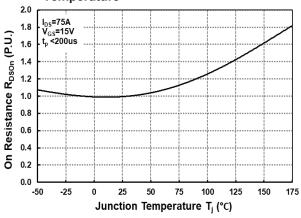


Fig 5. On-Resistance vs. Drain Current for Various Temperatures

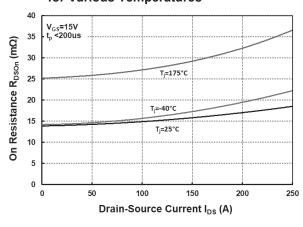
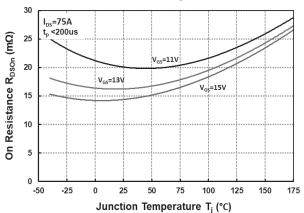


Fig 6. On-Resistance vs. Temperature for Various Gate Voltage



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Fig 7. Transfer Characteristic for Various Junction Temperatures

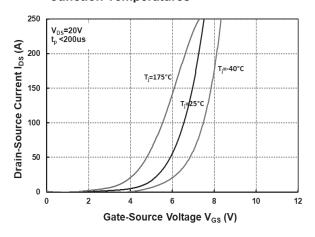


Fig 9. Body Diode Characteristics @ 25°C

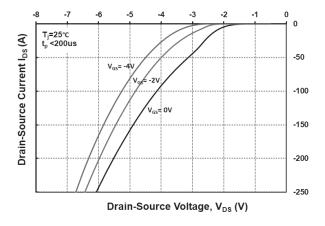


Fig 11. Threshold Voltage vs. Temperature

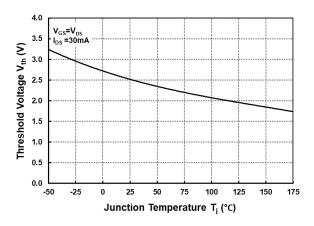


Fig 8.Body Diode Characteristics @ -40°C

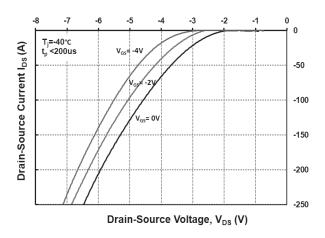


Fig 10. Body Diode Characteristics @ 175°C

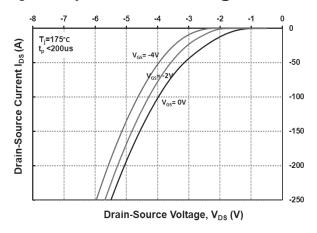
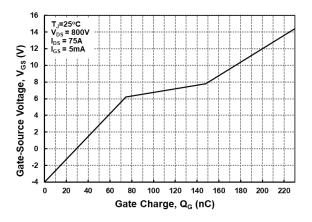


Fig 12. Gate Charge Characteristics



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Fig 13. 3rd Quadrant Characteristics @ -40°C

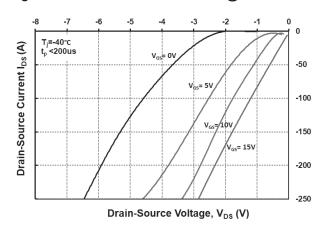


Fig 14. 3rd Quadrant Characteristics @ 25°C

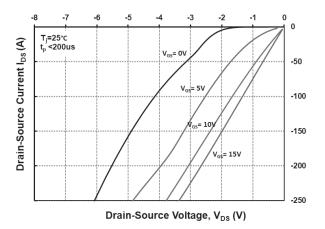


Fig 15. 3rd Quadrant Characteristics @ 175°C

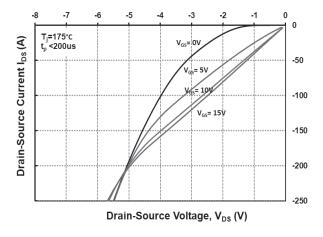


Fig 16. Output Capacitor Stored Energy

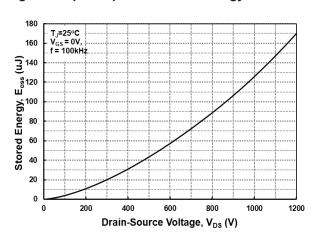


Fig 17. Capacitances vs. Drain-Source Voltage (0-200V)

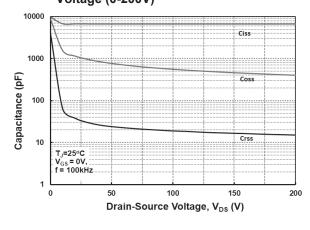
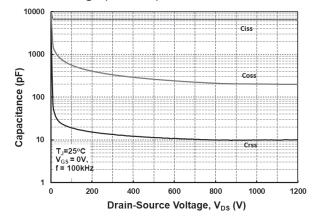


Fig 18. Capacitances vs. Drain-Source Voltage (0-1200V)



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Fig 19. Continuous Drain Current Derating vs. Case Temperature

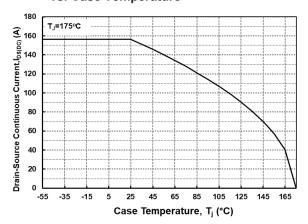


Fig 21. Transient Thermal Impedance (Junction – Case)

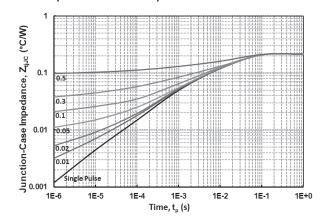


Fig 23. Clamped Inductive Switching Energy vs Drain Current (VDD = 800V)

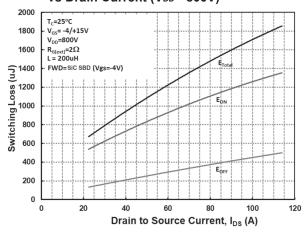


Fig 20. Maximum Power Dissipation Derating vs. Case Temperature

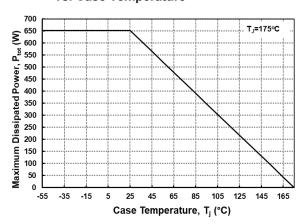


Fig 22. Safe Operating Area

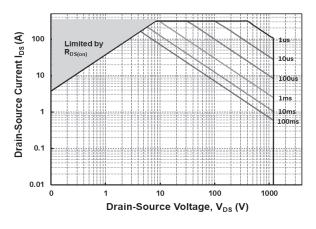


Fig 24. Clamped Inductive Switching Energy vs External Gate Resistor R_{G(ext)}

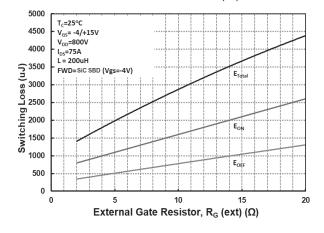




Fig 25. Switching Times vs Drain Current $(V_{DD} = 800V)$

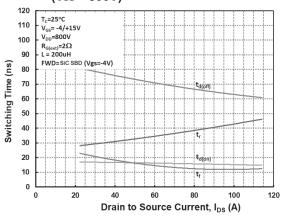
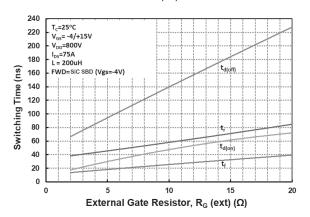


Fig 26. Switching Times vs External Gate Resistor R_{G(ext)}



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