MCV System on Module

MCV offers the full flexibility of the Altera Cyclone V SoC FPGA family. It integrates an ARM-based hard processor system (HPS) consisting of processor, peripherals, and memory interfaces with the FPGA fabric using a high-bandwidth interconnect backbone. The Altera SoCs combine the performance and power savings of hard intellectual property (IP) with the flexibility of programmable logic.

MCV featureset:

- Altera Cyclone V SoC FPGA
- Dual 800 MHz Cortex A9 Cores
- 256 Mbit Coniguration Device
- 143 FPGA signals
- 66 HPS signals
- programmable clock generator/ PLL
- HPS-Peripherals:
  - 1 GByte DDR3 RAM
  - 4 GByte eMMC memory
  - Gigabit Ethernet
  - UART
  - CAN
  - SPI
  - I2C
  - USB OTG
- single 3,3V supply
- 2 x Samtec QSH-090-01-F-D-A board-to-board connectors
- Size: 74mm x 42mm

OS Support:

- U-Boot
- Linux
- Yocto

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