

N-Channel 150-V (D-S) MOSFET

Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize RDS(ON), provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

- RDS(ON) = $60m\Omega$ @ Vgs = 10V
- Fast switching □
- Improve dv/dt Capability □
- 100% EAS Guaranteed □
- Green Device Available

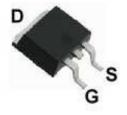
Typical Applications □

- Power Management in TV Converter
- Automotive Systems □
- White LED boost converters
- DC/DC Converter

Package type: TO-252

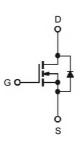
Packing & Order Information

3,000/Reel

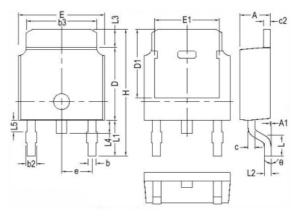


RoHS Compliant

Graphic Symbol

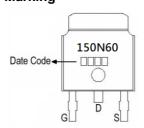


Package Dimension



REF.	Millimeter		REF.	Millimeter				
	Min.	Nom.	Max.	IXLI.	Min.	Nom.	Max.	
Α	2.20	2.30	2.38	E1	4.40	-	-	
A1	0	-	0.127	е	2.286 BSC			
b	0.64	0.76	0.88	Н	9.40	10.00	10.40	
b2	0.77	0.84	1.14	L	1.40	1.52	1.77	
b3	5.21	5.34	5.46	L1	2.743 Ref.			
С	0.45	0.50	0.60	L2	0.508 BSC			
c2	0.45	0.50	0.58	L3	0.89	-	1.27	
D	6.00	6.10	6.223	L4	0.64	-	1.01	
D1	5.21	-	-	L5	-	-	-	
E	6.40	6.60	6.731	θ	0 °	-	10°	

Marking





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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings					
Symbol	Parameter	Value	Units		
V _{DS}	Drain-Source Voltage	150	V		
V _G s	Gate-Source Voltage	±20	V		
	Continuous Drain Current ¹ (T _C =25°C)	23	Α		
I _D	Continuous Drain Current ¹ (T _C =100°C)	16	Α		
I _{DM}	Pulsed Drain Current ^{1,2}	60	Α		
las	Single Pulse Avalanche Current, L =0.1mH ³	18	Α		
Eas	Single Pulse Avalanche Energy, L =0.1mH ³	16.2	mJ		
	Power Dissipation ⁴ (T _A =25°C)	3.8	W		
P_D	Power Dissipation ⁴ (T _A =75°C)	2.5	W		
TJ/T _{STG}	Operating Junction and Storage Temperature	-55 to +150	°C		

Thermal Resistance Ratings					
Symbol	Parameter	Maximum	Units		
$R_{\theta JA}$	Maximum Junction-to-Ambient ¹	62.5	°C/W		
Rejc	Maximum Junction-to-Case ¹	2.1	°C/W		

Electrical Characteristics (Tյ=25°C unless otherwise specified)						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
$V_{\text{GS (th)}}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1.5	-	4	V
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	150	-	-	V
g fs	Forward Transconductance	V _{DS} =10V, I _D =5A	-	25	-	S
Igss	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =120V, V _{GS} =0V, T _J =25°C	_	-	1	μА
		V _{DS} =120V, V _{GS} =0V, T _J =55°C			10	
R _{DS} (on)	Drain-Source On-Resistance ²	V _{GS} =10V, I _D =10A	-		60	mΩ
		V_{GS} =4.5V, I_{D} =4.5A	-		70	11122
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V, L =0.1mH, I _{AS} =10A	5	-	-	mJ
V _{SD}	Diode Forward Voltage ²	IS = 1A,VGS = 0, V T _J =25°C	-	-	1.2	V
ls	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	-	-	20	Α

Notes

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 3. The EAS data shows maximum rating. The test condition is VDD=25V,VGS=10V,L=0.1mH,IAS=18A
- 4. The power dissipation is limited by 150 $\!\!\!^{\circ}\!\!\!^{\circ}$ junction temperature.
- 5. The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



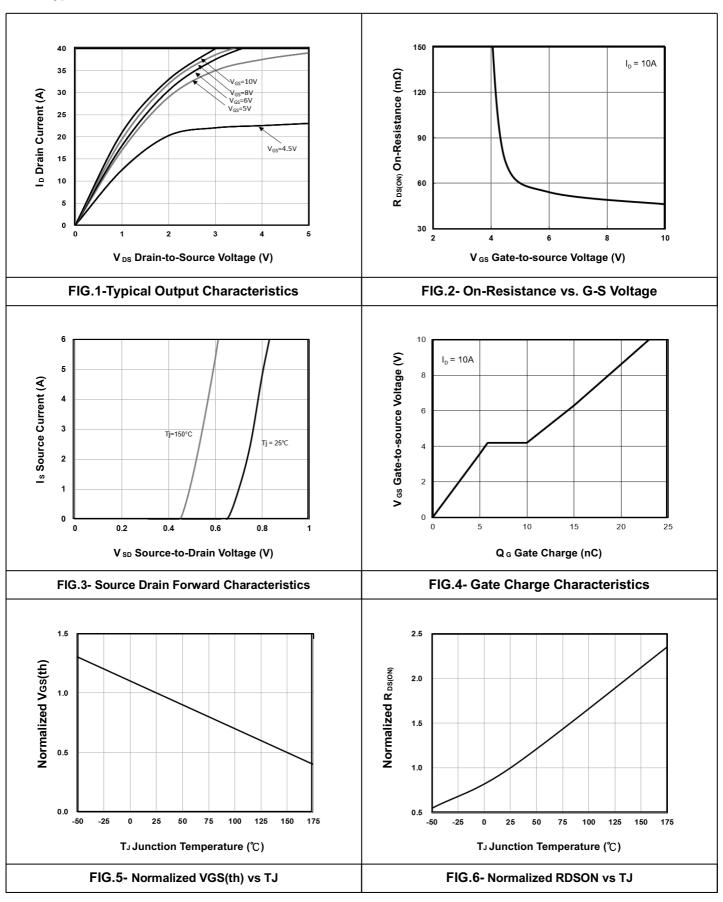
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Dynamic						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Qg	Total Gate Charge ²	VDS = 75 V,		23		
Qgs	Gate-Source Charge	VGS = 10 V,		5.8		nC
Q _{gd}	Gate-Drain ("Miller") Charge	ID = 10 A		4.2		
t _{d(on)}	Turn-On Delay Time ²			16.2		
tr	Rise Time	VDS = 75 V,		18.6		
t _{d(off)}	Turn-Off Delay Time	ID =10 A, VGS = 10 V, RG = 3.3 Ω		28.5		ns
t _f	Fall Time			6.5		
Ciss	Input Capacitance	VDS = 75 V,		1190		
Coss	Output Capacitance	VGS = 0 V,		73		pF
Crss	Reverse Transfer Capacitance	f = 1 Mhz		4		



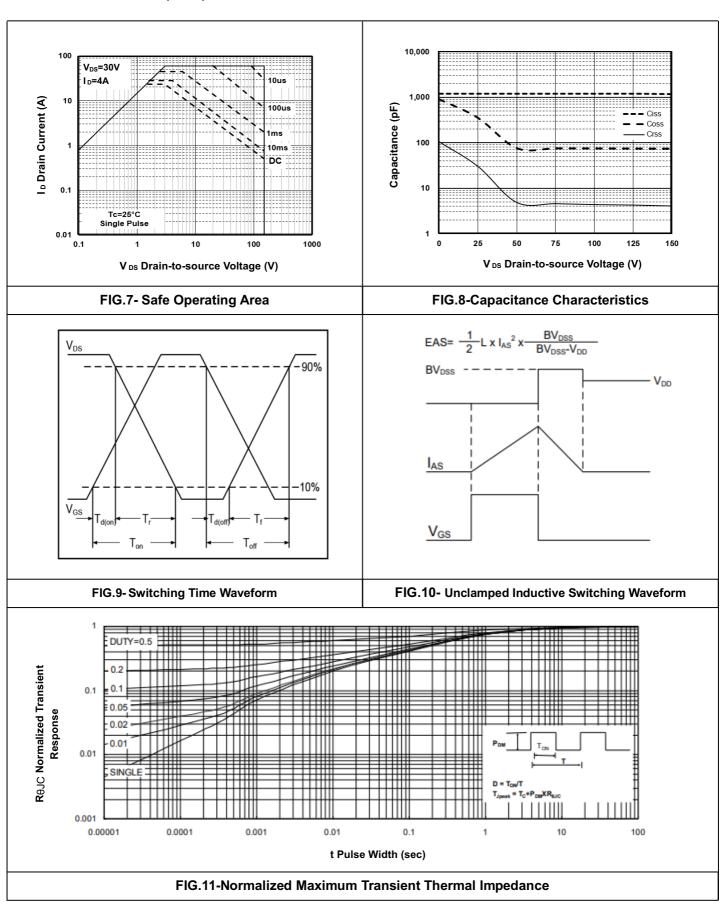
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• Typical Electrical Characteristics





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