

MSH100N045SAAU

N-Channel 100-V (D-S) MOSFET

Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize $R_{DS(ON)}$, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- $R_{DS(ON)} = 4.5m\Omega @ V_{GS} = 10V$
- Super Low Gate Charge
- Excellent dv/dt Capability
- 100% EAS Guaranteed
- Green Device Available

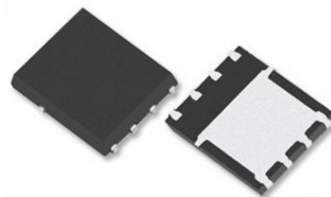
Typical Applications

- Motor Driver
- Load Switch
- Synchronous Rectifier
- BMS

Package type : PDFN 5X6

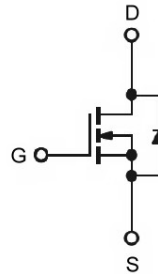
Packing & Order Information

3,000/Reel

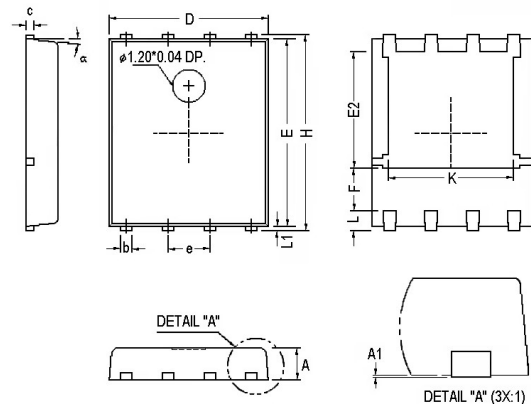


RoHS Compliant

Graphic Symbol

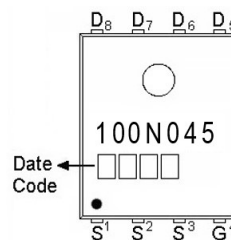


Package Dimension



REF.	Millimeter			REF.	Millimeter		
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	0.90	-	1.20	E	5.70	-	6.00
A1	0.00	-	0.10	e	-	1.27	-
b	0.30	-	0.50	H	5.90	-	6.30
c	0.20	-	0.30	L	-	0.60	-
D	4.80	-	5.40	L1	0.06	-	0.24
F	1.10 Ref.			α	0°	-	12°
E2	3.50	-	3.90	K	4.00	4.20	4.40

Marking



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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ¹ ($T_C = 25^\circ\text{C}$)	100	A
	Continuous Drain Current ¹ ($T_C = 100^\circ\text{C}$)	95	A
I_{DM}	Pulsed Drain Current ^{1,2}	480	A
I_{AS}	Single Pulse Avalanche Current, $L = 0.5\text{mH}^3$	53	A
E_{AS}	Single Pulse Avalanche Energy, $L = 0.5\text{mH}^3$	702	mJ
P_D	Power Dissipation ⁴ ($T_C = 25^\circ\text{C}$)	208	W
T_J/T_{STG}	Operating Junction and Storage Temperature	-55 to 175	$^\circ\text{C}$

Thermal Resistance Ratings

Symbol	Parameter	Maximum	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ¹	62	$^\circ\text{C/W}$
$R_{\theta JC}$	Maximum Junction-to-Case ¹	0.6	$^\circ\text{C/W}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	2	3	4	V
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$	100	-	-	V
I_{GSS}	Gate-Source Leakage Current	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
I_{DSS}	Drain-Source Leakage Current	$V_{DS} = 100\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 25^\circ\text{C}$	-	-	1	μA
		$V_{DS} = 100\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 125^\circ\text{C}$	-	-	10	
$R_{DS(on)}$	Static Drain-Source On-Resistance ²	$V_{GS} = 10\text{V}$, $I_D = 20\text{A}$	-	3.7	4.5	$\text{m}\Omega$
E_{AS}	Single Pulse Avalanche Energy ⁵	$V_{DD} = 25\text{V}$, $L = 0.5\text{mH}$, $I_{AS} = 20\text{A}$	100	-	-	mJ
V_{SD}	Diode Forward Voltage ²	$I_S = 50\text{A}$, $V_{GS} = 0\text{V}$, $T_J = 25^\circ\text{C}$	-	-	1.3	V
I_S	Continuous Source Current ^{1,6}	$V_G = V_D = 0\text{V}$, Force Current	-	-	100	A
I_{SM}	Pulsed Source Current ^{2,6}		-	-	200	

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Dynamic						
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Q _g	Total Gate Charge ²	V _{DS} = 50V	--	72	--	nC
Q _{gs}	Gate-Source Charge	I _D = 20A	--	28	--	
Q _{gd}	Gate-Drain Charge	V _{GS} = 10V	--	15	--	
t _{d(on)}	Turn-On Delay Time ²	V _{DD} = 50V	--	35	--	ns
t _r	Rise Time	I _D = 20A	--	18	--	
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10V	--	45	--	
t _f	Fall Time	R _G = 3.0Ω	--	55	--	
C _{iss}	Input Capacitance	V _{DS} = 50V	--	4725	--	pF
C _{oss}	Output Capacitance	V _{GS} = 0V	--	609	--	
C _{rss}	Reverse Transfer Capacitance	f = 1.0MHz	--	14	--	
R _g	Gate Resistance	V _{GS} = V _{DS} = 0V, f = 1.0MHz	--	1	--	Ω
t _{rr}	Reverse Recovery Time	I _F = 30A, dI/dt = 100A/μs, T _j = 25°C		70		nS
Q _{rr}	Reverse Recovery Charge			170		nC

Notes

1. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. The EAS data shows maximum rating. The test condition is V_{DD} = 25V, V_{GS} = 10V, L = 0.5mH, I_{AS} = 53A.
4. The power dissipation is limited by 175°C junction temperature.
5. The Min. value is 100% EAS tested guarantee.
6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

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- Typical Electrical Characteristics

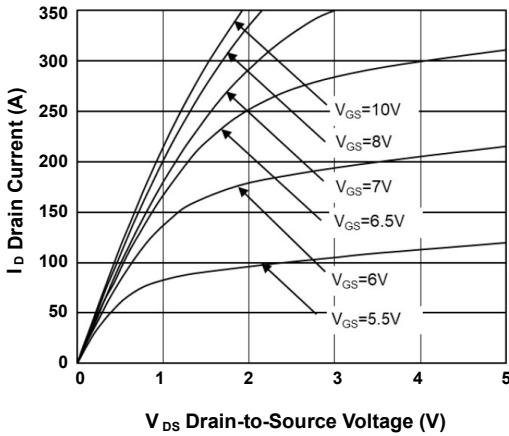


FIG.1-Typical Output Characteristics

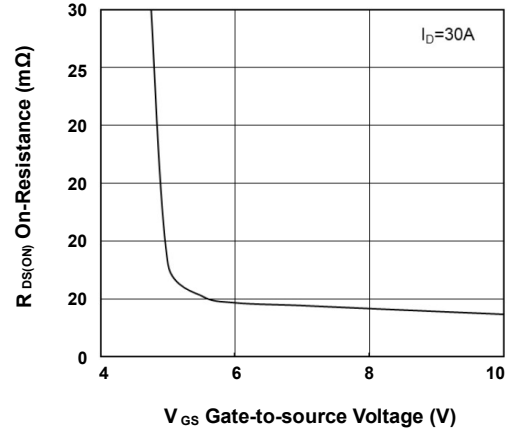


FIG.2-On-Resistance vs. G-S Voltage

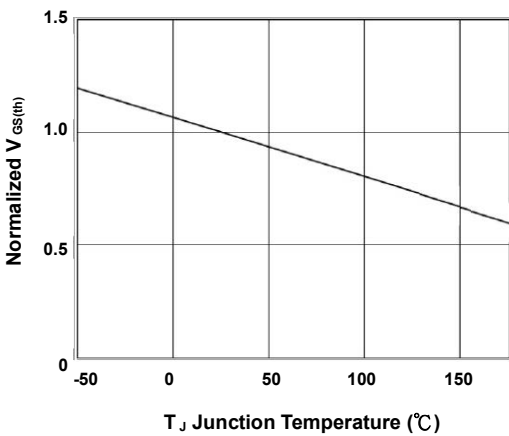


FIG.3-Normalized $V_{GS(th)}$ vs. T_J

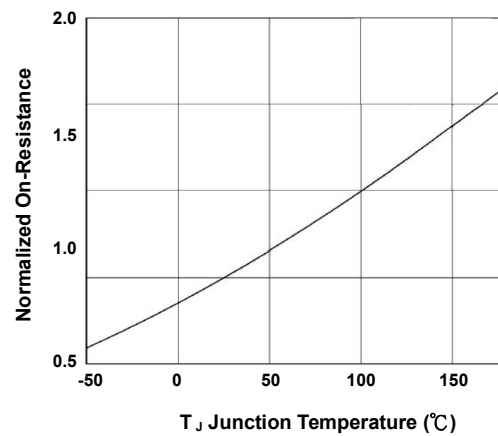


FIG.4-Normalized $R_{DS(on)}$ vs. T_J

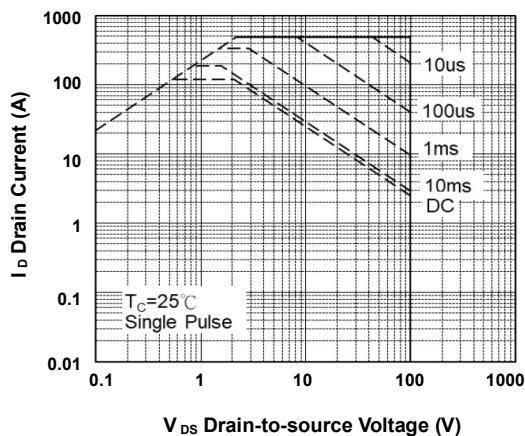


FIG.5-Safe Operating Area

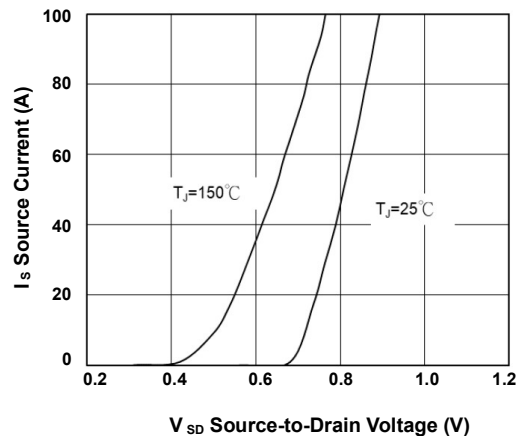


FIG.6-Source Drain Forward Characteristics

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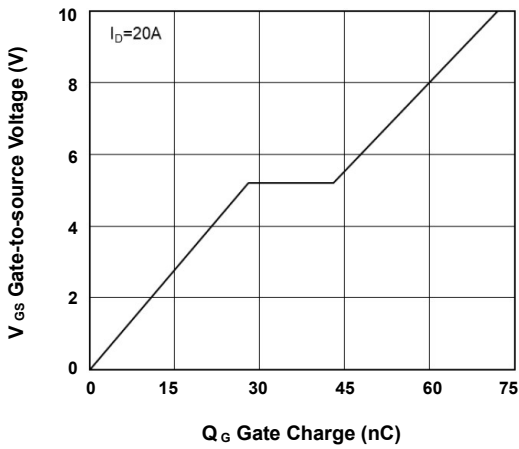


FIG.7-Gate Charge Characteristics

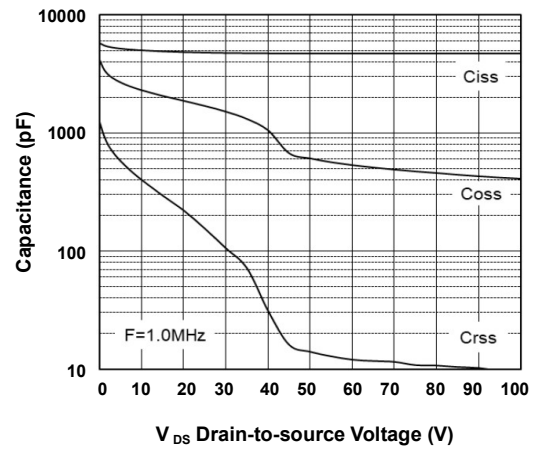


FIG.8-Capacitance Characteristics

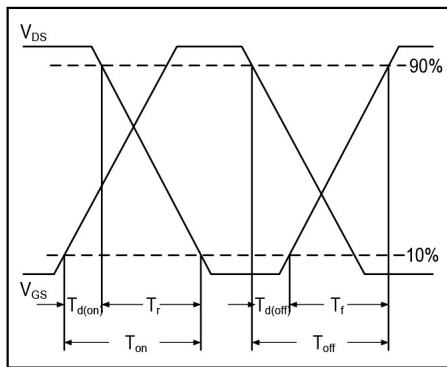


FIG.9-Switching Time Waveform

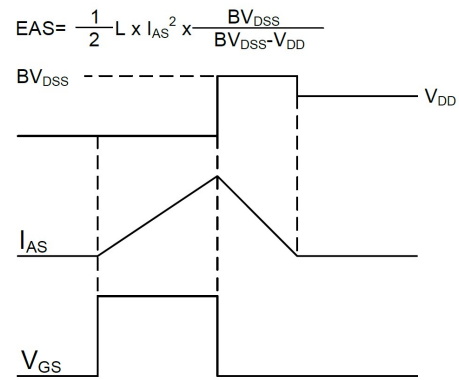


FIG.10-Unclamped Inductive Switching Waveform

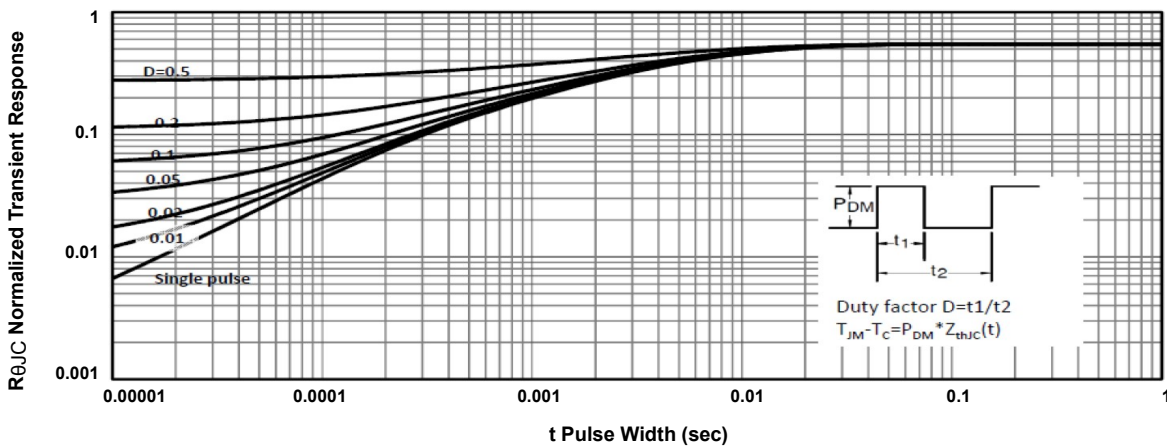


FIG.11-Normalized Maximum Transient Thermal Impedance

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