

Features

Small Voltage Offset

TPA1295: ±100 μV (Max)

Wide Common-Mode Voltage: −0.1 V to +70 V

Wide CMRR through Common Voltage: 130 dB (Typ)

• Supply Voltage: 3 V to 18 V

Accuracy and Zero-Drift Performance

±0.5% Gain Error (Max, −40°C to125°C)

0.5-µV/°C Offset Drift (Max, −40°C to 125°C)

5-ppm/°C Gain Drift (Max, -40°C to 125°C)

Three Gain Options for Voltage Output

TPA1295T: 20 V/VTPA1295F: 50 V/VTPA1295H: 100 V/V

Applications

- Current Sensing (High-Side/Low-Side)
- · Battery Chargers and Power Management
- Automotive
- Industrial Control and Automation
- Base Stations and Telecom Equipment

Description

The TPA1295 is a series of high-voltage, bi-directional current sense amplifiers with voltage output. The TPA1295 can sense drops across shunts at common-mode voltages from −0.1 V to 70 V. The TPA1295 is available with three output voltage scales: 20 V/V, 50 V/V, and 100 V/V, with up to 500-kHz bandwidth.

The TPA1295 operates from single 3-V to 18-V supply, and offers breakthrough performance throughout the temperature range from -40°C to $+125^{\circ}\text{C}$. It features a zero-drift core, which leads to a maximum offset drift of 0.5 $\mu\text{V}/^{\circ}\text{C}$ throughout the operating temperature range and the common-mode voltage range.

The TPA1295 series is offered in the SOP8 package.



Table of Contents

Features	1
Applications	1
Description	1
Revision History	3
Pin Configuration and Functions	4
Specifications	5
Absolute Maximum Ratings ⁽¹⁾	5
ESD, Electrostatic Discharge Protection	5
Thermal Information	5
Electrical Characteristics	6
Typical Performance Characteristics	7
Detailed Description	9
Overview	9
Application and Implementation	10
Application Information	10
Typical Application	11
Layout	12
Layout Guideline	12
Layout Example	12
Tape and Reel Information	13
Package Outline Dimensions	14
SOP8	14
Order Information	15
IMPORTANT NOTICE AND DISCLAIMER	16



Revision History

Date	Revision	Notes
2019-10-15	Rev.Pre.0	Initial version
2020-12-11	Rev.A.0	Released version
2022-05-01	Rev.A.1	Updated Order Information and Package Outline Dimensions
2024-12-17	Rev.A.2	The following updates are all about the new datasheet formats or typos, and the actual product remains unchanged. • Updated to a new datasheet format. • Added the MSL value in the Order Information. • Updated the Tape and Reel Information.
2025-6-19	Rev.A.3	Updated the Layout Information. Corrected handwriting errors.

www.3peak.com 3 / 16 AA20241202A3



Pin Configuration and Functions

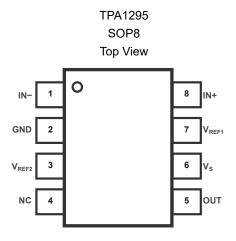


Table 1. Pin Functions: TPA1295

Р	in	Description
No.	Name	Description
1	IN-	Negative input
2	GND	Ground
3	V _{REF2}	Reference input 2
4	NC	Not connected
5	OUT	Output
6	Vs	Power supply
7	V _{REF1}	Reference input 1
8	IN+	Positive input

www.3peak.com 4 / 16 AA20241202A3



Specifications

Absolute Maximum Ratings (1)

Symbol	Parameter	Min	Max	Unit
	Supply Voltage		18	V
	Input Common Voltage (Continuous)	-0.3	75	V
	Input Current: +IN, –IN (2)	-10	10	mA
T _A	Operating Temperature Range	-40	125	°C
TJ	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range	-65	150	°C

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

ESD, Electrostatic Discharge Protection

Symbol	Parameter	Condition	Minimum Level	Unit
НВМ	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001 (1)	2	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002 (2)	1.5	kV

⁽¹⁾ JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

Thermal Information

Package Type	θ _{JA}	θυς	Unit
SOP8	158	43	°C/W

www.3peak.com 5 / 16 AA20241202A3

⁽²⁾ The inputs are protected by ESD protection diodes to the power supply.

⁽²⁾ JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.



Electrical Characteristics

All test conditions: V_S = 4.5 V to 5.5 V, V_{IO} = 3.0 V to 5.5 V, T_A = -40°C to 125°C, unless otherwise noted.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Input						
Vos	Input Offset Voltage			±10	±100	μV
Vos TC (1)	Input Offset Voltage Drift	-40°C to 125°C			0.5	μV/°C
V _{CM}	Common-Mode Input Range	-40°C to 125°C	-0.1		70	V
CMRR	Common-Mode Rejection Ratio	-40°C to 125°C, -0.1 V < V _{CM} < 70 V	110	130		dB
		DC to 50 kHz		90		dB
	1 18: 0 1	-40°C to 125°C, V _{CM} = 12 V		12		μA
l _Β	Input Bias Current	-40°C to 125°C, V _{CM} = 70 V			210	μA
los	Input Offset Current	-40°C to 125°C			1	μA
PSRR	Power Supply Rejection Ratio	3.0 V < (+V _S) < 5.5 V		110		dB
Noise RTI	(2)					
en	Input Voltage Noise Density	f = 1 kHz		55		nV/√ Hz
Output						
	Gain	TPA1295T		20		V/V
G		TPA1295F		50		V/V
		TPA1295H		100		V/V
GE	Gain Error	-40°C to 125°C		±0.1	±0.5	%
GE TC	Gain Error vs. Temperature	-40°C to 125°C		3	5	ppm/°C
CLOAD	Maximum Capacitive Load	No oscillation		0.5		nF
V_{OH}	Output Swing from Supply Rail	-40°C to 125°C, Source 500 μA		0.1	0.31	V
V_{OL}	Output Swing from Supply Rail	-40°C to 125°C, Sink 500 μA		0.01	0.02	V
Frequence	Response					
BW	Bandwidth	All gain configuration		500		kHz
SR	Slew Rate	$V_{SENSE} = V_{IN+} - V_{IN-} = 500 \text{ mV}$		20		V/µs
Power Su	oply					
Vs	Supply Voltage		3		18	V
IQ	Quiescent Current			600	1000	μA
Temperatu	ire Range					
	Specified Range		-40		125	°C

⁽¹⁾ Maximum specification is calculated with a limited sample quantity in the laboratory.

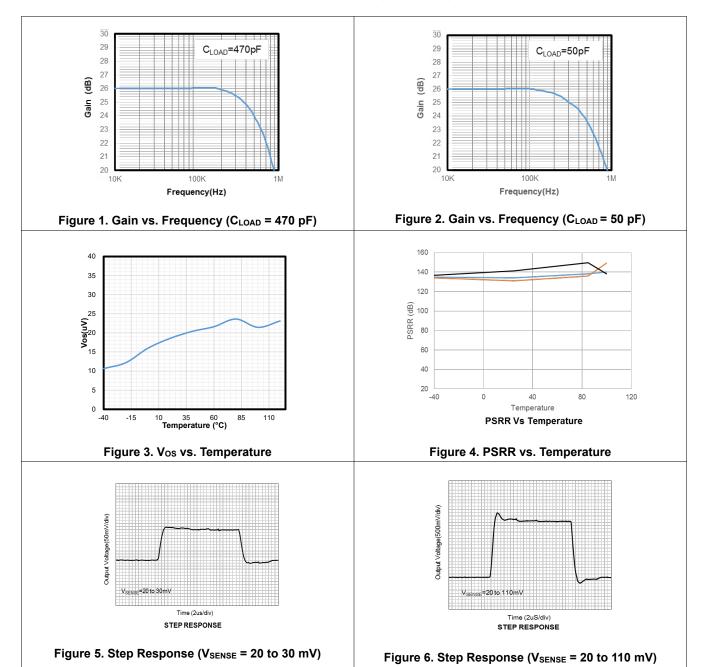
www.3peak.com 6 / 16 AA20241202A3

⁽²⁾ RTI = referred to input.

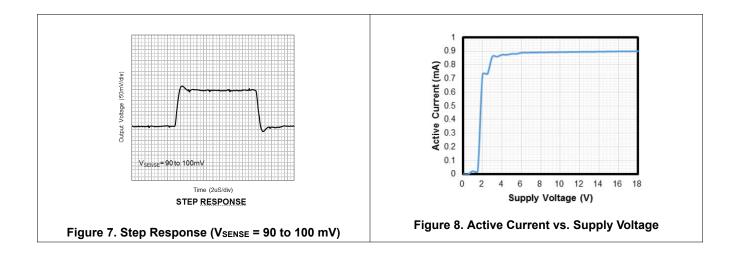


Typical Performance Characteristics

All test conditions: $T_A = 25$ °C, $V_{SENSE} = V_{IN+} - V_{IN-} = 1$ mV, $V_S = 12$ V, $V_{IN+} = 24$ V, unless otherwise noted.







www.3peak.com 8 / 16 AA20241202A3



Detailed Description

Overview

The TPA1295 is a series of high-voltage power-supply, zero-drift, differential amplifiers that use a unique architecture to accurately amplify small differential current shunt voltages, especially for fast-changing common-mode voltages. In typical applications, the TPA1295 series measures current by amplifying the voltage across a shunt resistor connected to its inputs by 3 gains of 20 V/V, 50 V/V, and 100 V/V. The design provides excellent common-mode rejection, even with PWM common-mode inputs that can change at very fast rates. The TPA1295 features an input offset less than 100 μ V, and an offset drift is less than 500 nV/°C.

www.3peak.com 9 / 16 AA20241202A3



Application and Implementation

Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

Reference Pin Connection for Bi-directional and Unidirectional Current Measurements

The output voltage is set by applying a voltage to the reference voltage inputs, REF1 and REF2. The output of the TPA1295 can be adjusted for unidirectional or bidirectional operation. In unidirectional operation, the output can be set at the negative rail (near ground) or at the positive rail (near V_S) when the differential input is 0 V, and both reference inputs are connected to ground or positive supply. In bi-directional operation, the output is set within the output range. Adjusting the output is accomplished by applying voltage(s) to the referenced inputs, V_{REF1} and V_{REF2} . The output when there is no differential input can be calculated by Equation 1.

$$V_{\text{out}} = (V_{\text{REF1}} + V_{\text{REF2}}) \times 5/12 \tag{1}$$

Selecting R_{sense}

The zero-drift offset performance of the TPA1295 offers several benefits. Most often, the primary advantage of the low offset characteristic enables lower full-scale drops across the R_{sense}. For example, non-zero-drift current sense monitors typically require a full-scale range of 100 mV. The TPA1295 family gives equivalent accuracy at a full-scale range from approximately 5 to 10 mV. This accuracy reduces R_{sense} dissipation by an order of magnitude with many additional benefits.

Alternatively, there are applications that must measure current over a wide dynamic range that can take advantage of the low offset at the low end of the measurement. Most often, these applications can use the lower gains of the TPA1295 to accommodate larger R_{sense} drops at the upper end of the scale.

Recommended Component Values

Ideally, the maximum load current develops the full-scale sense voltage across the current-sense resistor. Choose the gain needed to match the maximum output voltage required for the application:

$$V_{\text{out}} = V_{\text{sense}} \times A_{V}$$
 (2)

Where V_{sense} is the full-scale sense voltage, and A_V is the gain of the TPA1295.

In applications of monitoring a high current, ensure that R_{sense} is able to dissipate its own I^2R power loss. If the power dissipation of the resistor exceeds the nominal value, its value may drift, or it may fail altogether. The TPA1295 senses a wide variety of currents with different sense-resistor values.

Power Supply Recommendation

The input circuitry of the TPA1295 can accurately measure beyond its power-supply voltage, $+V_S$. For example, the $+V_S$ power supply can be 5 V, whereas the load power-supply voltage can be as high as 70 V. However, the output voltage range of the OUT pin is limited by the voltages on the power-supply pin.

www.3peak.com 10 / 16 AA20241202A3



Typical Application

Figure 9 shows the typical application schematic.

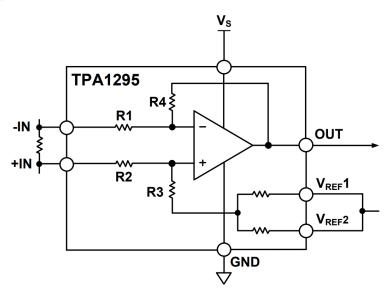


Figure 9. Typical Application Circuit

www.3peak.com 11 / 16 AA20241202A3



Layout

Layout Guideline

- Because the high currents may flow through R_{sense} based on the application, take care to eliminate solder and parasitic
 trace resistance from causing errors in the sense voltage. Either use a four-terminal current sense resistor or use Kelvin
 (force and sense) PCB layout techniques.
- Make sure the sense resistor has as much copper trace area as possible to dissipate heat as the resistor value changes slightly with temperature. Also, see the resistor manufacturer datasheet or application notes for further layout guidelines.
- The power-supply bypass capacitor should be placed as close as possible to the supply and ground. The recommended value of this bypass capacitor is 0.1 μF. Additional decoupling capacitance can be added to compensate for noisy or high-impedance power supplies.

Layout Example

Figure 10 shows the location of external components as they appear on the PCB diagram.

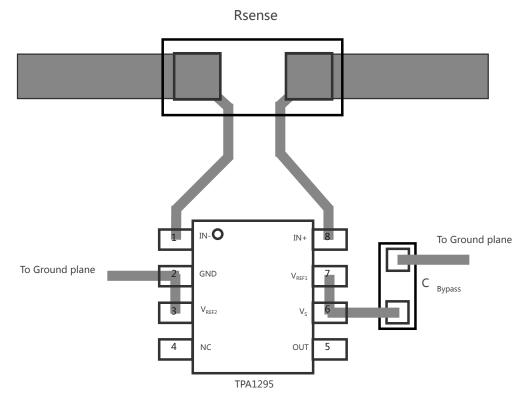
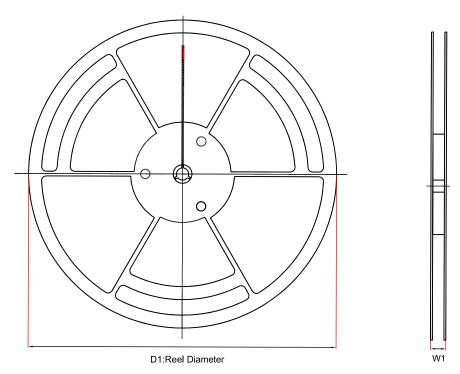


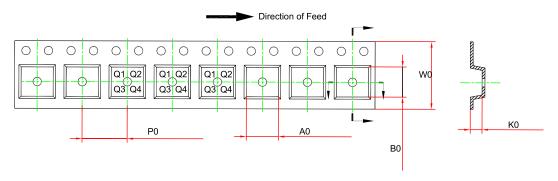
Figure 10. Recommended Layout

www.3peak.com 12 / 16 AA20241202A3



Tape and Reel Information





Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm) ⁽¹⁾	B0 (mm) ⁽¹⁾	K0 (mm) ⁽¹⁾	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPA1295T-SO1R-S	SOP8	330.0	17.6	6.4	5.4	2.1	8.0	12.0	Q1
TPA1295F-SO1R-S	SOP8	330.0	17.6	6.4	5.4	2.1	8.0	12.0	Q1
TPA1295H-SO1R-S	SOP8	330.0	17.6	6.4	5.4	2.1	8.0	12.0	Q1

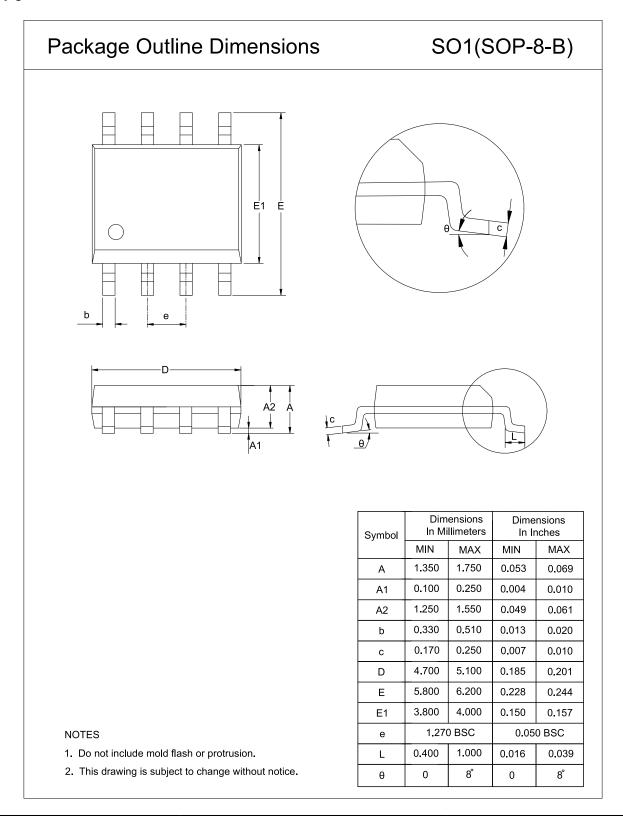
(1) The value is for reference only. Contact the 3PEAK factory for more information.

www.3peak.com 13 / 16 AA20241202A3



Package Outline Dimensions

SOP8





Order Information

Order Number	Operating Temperature Range	Gain	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPA1295T-SO1R-S (1)	–40°C to 125°C	20 V/V	SOP8	295T	3	Tape and Reel, 4,000	Green
TPA1295F-SO1R-S	–40°C to 125°C	50 V/V	SOP8	295F	3	Tape and Reel, 4,000	Green
TPA1295H-SO1R-S (1)	–40°C to 125°C	100 V/V	SOP8	295H	3	Tape and Reel, 4,000	Green

⁽¹⁾ For future products, contact the 3PEAK factory for more information and samples.

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

www.3peak.com 15 / 16 AA20241202A3



IMPORTANT NOTICE AND DISCLAIMER

Copyright[©] 3PEAK 2012-2025. All rights reserved.

Trademarks. Any of the 思瑞浦 or 3PEAK trade names, trademarks, graphic marks, and domain names contained in this document /material are the property of 3PEAK. You may NOT reproduce, modify, publish, transmit or distribute any Trademark without the prior written consent of 3PEAK.

Performance Information. Performance tests or performance range contained in this document/material are either results of design simulation or actual tests conducted under designated testing environment. Any variation in testing environment or simulation environment, including but not limited to testing method, testing process or testing temperature, may affect actual performance of the product.

Disclaimer. 3PEAK provides technical and reliability data (including data sheets), design resources (including reference designs), application or other design recommendations, networking tools, security information and other resources "As Is". 3PEAK makes no warranty as to the absence of defects, and makes no warranties of any kind, express or implied, including without limitation, implied warranties as to merchantability, fitness for a particular purpose or non-infringement of any third-party's intellectual property rights. Unless otherwise specified in writing, products supplied by 3PEAK are not designed to be used in any life-threatening scenarios, including critical medical applications, automotive safety-critical systems, aviation, aerospace, or any situations where failure could result in bodily harm, loss of life, or significant property damage. 3PEAK disclaims all liability for any such unauthorized use.

www.3peak.com 16 / 16 AA20241202A3